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<tr>
<td>May 2017</td>
<td>2.2</td>
<td>Minor document revision, corrected formatting and wording in several sections.</td>
</tr>
<tr>
<td>January 2016</td>
<td>2.1</td>
<td>Removed deprecated content from former section 7, merged former section 6 into section 5. Updated the supported OS list.</td>
</tr>
<tr>
<td>December 2016</td>
<td>2.0</td>
<td>&quot;Workarounds&quot; section has been updated. Removed Microsoft Windows* from supported OS list.</td>
</tr>
<tr>
<td>November 2016</td>
<td>1.9</td>
<td>&quot;Workarounds&quot; section has been updated. Clarified information DTS tools.</td>
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<td>November 2016</td>
<td>1.8</td>
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<tr>
<td>August 2016</td>
<td>1.7</td>
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<td>1.6</td>
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<td>July 2016</td>
<td>1.5</td>
<td>Updated install section to contain information how to handle early ship software versions.</td>
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<tr>
<td>June 2016</td>
<td>1.4</td>
<td>Corrected trademarks.</td>
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<tr>
<td>April 2016</td>
<td>1.3</td>
<td>Updated known-issues section.</td>
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<tr>
<td>February 2016</td>
<td>1.2</td>
<td>Added Microsoft Windows* support information.</td>
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<tr>
<td>December 2015</td>
<td>1.1</td>
<td>Fixed OS table support.</td>
</tr>
<tr>
<td>December 2015</td>
<td>1.0</td>
<td>Initial official version.</td>
</tr>
<tr>
<td>September 2015</td>
<td>0.5</td>
<td>Draft revision for review.</td>
</tr>
</tbody>
</table>
1 Introduction

Intel® Xeon Phi™ Processor Software is a set of software and utilities that enable functionalities of the Intel® Xeon Phi™ processor x200. This document will allow its readers to understand and utilize those features.

Please note that this document pertains only to systems containing at least one Intel® Xeon Phi™ processor x200.

1.1 Terminology

<table>
<thead>
<tr>
<th>Term</th>
<th>Description</th>
</tr>
</thead>
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<tr>
<td>DTS</td>
<td>Developer Tool Set</td>
</tr>
<tr>
<td>Upstream kernel</td>
<td>The Linux* kernel source code from <a href="http://www.kernel.org">www.kernel.org</a></td>
</tr>
<tr>
<td>gcc</td>
<td>The GNU C Compiler collection</td>
</tr>
<tr>
<td>gdb</td>
<td>The GNU Debugger</td>
</tr>
<tr>
<td>EDAC</td>
<td>Error Detection and Correction infrastructure in Linux kernel, Its purpose is to detect problems with the hardware in a system running Linux*.</td>
</tr>
<tr>
<td>PM</td>
<td>Power Management</td>
</tr>
<tr>
<td>PMU</td>
<td>Performance Monitoring Unit, is a set of counters used to understand events happening inside a CPU</td>
</tr>
<tr>
<td>MCDRAM</td>
<td>High Bandwidth memory found in the processor package.</td>
</tr>
<tr>
<td>MCE</td>
<td>Machine Check Exception</td>
</tr>
<tr>
<td>memkind</td>
<td>Helper library allows direct memory allocations in the MCDRAM.</td>
</tr>
</tbody>
</table>

1.2 Supported operating systems

Intel® Xeon Phi™ Processor Software supports Linux* Kernel-based operating systems. Refer to Section 3.1 for a list of Linux* distributions that Intel® Xeon Phi™ Processor Software was validated against.
2 Intel® Xeon Phi™ Processor Software Content

2.1 Kernel

Linux* Kernel delivered with Intel® Xeon Phi™ Processor Software is based on an OS distribution kernel. Intel® Xeon Phi™ Processor Software also contains specific additions in form of patches which enable different core functionalities of the Intel® Xeon Phi™ processor x200. These functionalities are described further in this document.

2.2 Kernel tools

Please note that the kernel-tools package is only delivered for Red Hat* Linux* 7.2 distribution. It consists of the following tools:

- cpupower - shows and sets processor power related values
- turbostat - reports processor frequency and idle statistics
- x86_energy_perf_policy - read or write MSR_IA32_ENERGY_PERF_BIAS

2.3 Cpupower package

Please note that the cpupower package is only delivered for SUSE* Linux* Enterprise Server 12.1 distribution. It consists of the following tools:

- cpupower - shows and sets processor power related values
- turbostat - reports processor frequency and idle statistics

2.4 The cpuid Package

Cpuid is a user space tool that provides an interface for querying information about the x86 CPU.

2.5 The hwloc Package

The Portable Hardware Locality (hwloc) software package provides a portable abstraction (across OS, versions, architectures, etc) of the hierarchical topology of modern architectures, including NUMA memory nodes, shared caches, processor sockets, processor cores and processing units (logical processors or "threads"). It also gathers various system attributes such as cache and memory information. It primarily aims at helping applications with gathering information about modern computing hardware so as to utilize it accordingly and efficiently. Hwloc may display the topology in multiple convenient formats. It also offers a powerful programming interface (C API) to gather information about the hardware, bind processes, and much more.
2.6 The mcelog Package

mcelog is a utility that collects and decodes Machine Check Exception data. It can be run either as a daemon, or by cron. More detailed information about mcelog can be found at

http://www.mcelog.org/


Mcelog coexists in system with the EDAC driver. Both mechanisms work independently, although their functionalities may overlap. Both tools were enabled for the KNL platform and their output was validated. The choice which system should be used depends on the needs and expectations of the system administrator. While mcelog is more flexible by giving the user possibility to configure some options, EDAC, as a part of kernel, can be considered more reliable. Having both components activated at the same time is also possible. If the system has both components up, configured and running, each memory error should be reported by both mcelog and EDAC. By default EDAC outputs errors to the kernel ring buffer (dmesg) while mcelog appends them to the syslog (/var/log/messages).

Status of each component can be checked using below commands:

- for mcelog (status of the mcelog service should be "active (running)"):  
  $ service mcelog status

- for EDAC (both edac_core and sb_edac modules should be loaded):  
  $ lsmod | grep edac

2.7 The memkind Library

The memkind library is a user-extensible heap manager, designed to provide efficient allocation mechanism for multithreaded applications and support for high bandwidth memory (MCDRAM). The memkind library is built on top of jemalloc and enables partitioning of the heap between kinds of memory in NUMA-capable systems.

There are several strategies (memkind kinds) of heap management provided out-of-the-box by the library, such as allocating from standard or high bandwidth memory, as well as using standard or huge pages (both 2 MB and 1 GB sizes).

Heap management strategy can be adjusted either by using one of the predefined kinds exampled above, or user-created ones, which address application specific needs. More information about the predefined kinds and creating custom ones can be found in the memkind manual (sections KINDS and MEMKIND OPERATIONS) and memkind examples ("new_kind_example.c").

The memkind library provides full compatibility with ISO C standard APIs.

The high bandwidth memory interface (the hbwmalloc API) is a set of standard heap management functions such as malloc, calloc, realloc and free, prefixed by hbw_. This API also provides hbwmalloc_allocator class compatible with the C++ standard library allocator concepts, and features policy that determines behavior when there is not enough free high bandwidth memory to satisfy a user’s request. To find out more about the hbwmalloc API please refer to its man page.
The standard *memkind* API provides a set of standard heap management functions, each one prefixed by *memkind_* and with additional parameter to specify the *kind*. The standard API also includes functionality for *kind* management, error handling and debugging. To find out more about the *memkind* API please refer to its man page.

Further instructions on installing and using the memkind library can be found in its README file.

For additional information please refer to the Intel® Xeon Phi™ Processor Programming and Leveraging High Bandwidth Memory whitepaper rev. 0.5. This document can be downloaded from Intel® IPS or CDI Doc #570827.

The source code repositories, and additional information can be found at [http://memkind.github.io/memkind/](http://memkind.github.io/memkind/).

### 2.8 The micperf Package

*Micperf* is designed to incorporate a variety of benchmarks into a simple user experience with a single interface for execution and a unified means of data inspection. The user interface consists of five executables: one for execution of benchmarks (*micprun*), and four that interpret the output of the first one. The results can be displayed as professional quality plots, human readable text or comma separated value output that can be easily imported into a variety of other applications.

The *micprun* executable, the primary application in the *micperf* package, executes six benchmarks: MKL [3] SMP Linpack, MKL HPLinpack [4], MKL HPCG [5], MKL SGEMM, MKL DGEMM, and STREAM [6], [7]. These benchmarks were carefully chosen to demonstrate performance in all of the major bottlenecks in the system.

### 2.9 The systools-sb Package

The *Systools-sb* package contains the *SysDiag* tool which provides a variety of information and diagnostics for the processor. *SysDiag* also monitors DDR, MCDRAM and PCI-E information. It also monitors temperature and performance state data of the CPU.

For detailed information execute the *SysDiag* tool help.
This chapter describes how Intel® Xeon Phi™ Processor Software can be installed and configured.

**Note:** It is strongly recommended to read through this chapter before actually proceeding with installation to ensure that all required components and facilities are available. It is also strongly recommended that these installation steps be performed in the order they are presented.

**Note:** All software packages provided for the Intel® Xeon Phi™ processor x200 are prefixed with the `xppsl` label. This document assumes that the system does not contain an early ship version of the software, which might have been labelled differently. It is necessary to remove any early ship packages from your system before following the steps below. Instructions on how to remove those packages are provided in the early ship software user’s guide.

### 3.1 Prerequisites

It is necessary that your system contains at least one Intel® Xeon Phi™ processor x200.

Intel® Xeon Phi™ Processor Software has been validated against specific versions of CentOS*, Red Hat* Enterprise Linux* and SUSE* Linux* Enterprise Server (SLES*) as the main operating system. **Table 1** lists the validated versions of these operating systems.

#### Table 1 Validated Host Operating Systems (Linux*)

<table>
<thead>
<tr>
<th>Supported OS Versions</th>
<th>Kernel Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>CentOS* 7.2</td>
<td>kernel-3.10.0-327.36.3.el7.xppsl_1.5.2</td>
</tr>
<tr>
<td>CentOS* 7.3</td>
<td>kernel-3.10.0-514.el7</td>
</tr>
<tr>
<td>Red Hat* Enterprise Linux* 7.3</td>
<td>kernel-3.10.0-514.el7</td>
</tr>
<tr>
<td>SUSE* Linux* Enterprise Server 12 SP1</td>
<td>kernel-default-3.12.66-60.64.8.273.g9e1b23.xppsl_1.5.2</td>
</tr>
<tr>
<td>SUSE* Linux* Enterprise Server 12 SP2</td>
<td>kernel-default-4.4.21-69.1</td>
</tr>
</tbody>
</table>

To obtain the version of the kernel running on the host, execute:

```
$ uname -r
```
Note: Some packages that will be installed require access to the standard distribution packages and repositories. If you disabled any of the standard repositories please consider re-enabling them to prevent failed dependencies issues. To get more information please check the information provided by your operating system documentation – for Red Hat* Enterprise Linux*, and for SUSE* Linux* Enterprise Server.

3.2 Root Access

Many of the tasks described in this document require administrative access privileges (i.e. root access). Verify that you have such privileges to the machines you will configure.

The use of sudo to acquire root privileges should be done carefully because its use may cause subtle and undesirable side effects. Sudo might not retain the non-root environment of the caller. This could, for example, result in use of a different PATH variable than expected, ending up with execution of the wrong code.

When su is used to become root, the non-root environment is (mostly) retained. (HOME, SHELL, USER, LOGNAME are reset unless the -m switch is given. See the su man page for details).

3.3 Distribution packages replacement

Please note, that installing Intel® Xeon Phi™ Processor Software may replace some of pre-installed packages that come with your OS distribution. Packages that may be replaced are listed below:

- cpuid
- cpupower
- hwloc
- mcelog
- memkind

3.4 Installation

The following process will not replace your current Linux* kernel. In some Linux* distributions the installation may add a new kernel to grub, so it is be possible to choose the Intel® Xeon Phi™ Processor Software kernel on startup. In such cases, the newly installed kernel contains information about Intel® Xeon Phi™ Processor Software version, possible kernel names are described in Table 1.

3.4.1 Get the Intel® Xeon Phi™ Processor Software distribution

The latest Intel® Xeon Phi™ Processor Software distribution can be obtained from the http://www.software.intel.com website. The software package releases are available in separate tar files for each supported OS. Download a package for your operating system.
After downloading, extract the release package:

```bash
$ tar xvf xppsl-<version>-<os>.tar
$ cd xppsl-<xppsl-version>/
```

### 3.4.2 Intel® Xeon Phi™ Processor Software Upgrade

Intel® Xeon Phi™ Processor Software supports automated updates since version 1.1.2. If you are on version 1.1.2 or above use `yum` or `zypper` to perform an update as described in Section 3.4.4.

### 3.4.3 Intel® Xeon Phi™ Processor Software Uninstall

To check for a previously installed version of Intel® Xeon Phi™ Processor Software execute:

```bash
$ rpm -qa | grep xppsl
```

Packages that correlate with Intel® Xeon Phi™ Processor Software will be listed and have to be uninstalled:

- **Red Hat* Enterprise Linux*/CentOS***:
  ```bash
  # yum remove [package-name]
  ```

- **SUSE* Linux* Enterprise Server**
  ```bash
  # zypper rm [package-name]
  ```

### 3.4.4 Intel® Xeon Phi™ Processor Software Installation

- **Red Hat* Enterprise Linux*/CentOS***:
  ```bash
  $ cd rhel<os-version>/
  Install RPMs:
  $ yum install x86_64/*rpm
  ```

- **CentOS***:
  ```bash
  $ cd centos<os-version>/
  Install RPMs:
  $ yum install x86_64/*rpm
  ```

- **SUSE* Linux* Enterprise Server 12.1**:
  ```bash
  $ cd suse<os-version>/
  Install RPMs:
  $ zypper install noarch/*rpm x86_64/*rpm
  ```
SUSE* Linux* Enterprise Server 12.2:

$ cd suse<os-version>/

Install RPMs:

$ zypper install x86_64/*rpm

**Note:** In rare cases zypper might not be able to find all dependencies returning a *Failed dependencies* error message. The solution to this issue is manual installation of the missing software:

$ cd suse<os-version>
$ zypper install noarch/kernel-macros-3.12.28-4.6.xppsl_1
<version>.noarch
$ kernel-devel-3.12.28-4.6.xppsl_<version>.noarch.rpm
$ zypper install noarch/*rpm x86_64/*rpm

If the following error occurs:

“The selected package 'kernel-devel-3.12.28-4.6.xppsl_'<version>.noarch' from repository 'Plain RPM files cache' has lower version than the installed one.”

Please use the command below to force install the package.

$ zypper install --oldpackage kernel-devel-3.12.28-
4.6.xppsl_<version>.noarch

**Note:** Update the additional *devel* and *debuginfo* packages if they were installed with the previous version of the software. Not updating these packages will result in dependency conflicts when running the commands above.
4 Rebuilding Intel® Xeon Phi™ Processor Software based Package Locally

Typically an RPM file is pre-compiled and ready for direct installation. The corresponding source code can also be distributed. This is done in an SRPM package, which also includes the SPEC file describing the software and how it is built. The SRPM also allows the user to compile and modify the code.

The source code for user space tools is included in Intel® Xeon Phi™ Processor Software for both Red Hat* Enterprise Linux* and SUSE* Linux* Enterprise Server. The quickest way to handle the *.src.rpm files is to use the rpmbuild command. Please follow steps described below for instructions on rebuilding RPM files.

Go to your Intel® Xeon Phi™ Processor Software directory:

CentOS*:

$ cd centos*/srpms/

Red Hat* Enterprise Linux*:

$ cd rhel*/srpms/

SUSE* Linux* Enterprise Server:

$ cd suse*/srpms/

Build the RPM package with the following command:

$ rpmbuild --rebuild <source_rpm_file>

§
The Intel® Xeon Phi™ Processor x200 platform-specific features have been enabled in both Linux* upstream kernel and vendor kernels, therefore, provided the system was set up in accordance to this guide, user should be able to fully utilize the hardware. However, some issues cannot be directly addressed in kernel, or the solution cannot be upstreamed for some reason. This chapter describes such problems and shows possible ways to eliminate or mitigate their consequences.

5.1 General issues

1. Package debuginfo type conflicts with distribution/upstream packages
2. Package xppsl-hwloc-devel requires enabling RHEL* 7 subscription.
3. Package xppsl-hwloc-devel can break the Intel® Xeon Phi™ Processor Software installation due to missing dependencies pkgconfig(libpciaccess) and pkgconfig(libxml-2.0).

This dependencies cannot be satisfied by using the RHEL* 7 DVD. Registering and enabling RHEL* subscription is required. To enable subscription please refer to the RHEL* 7 user guide.

4. Package xppsl-hwloc does not update and needs to be reinstalled manually.
5. The hwloc memory side cache discovery might fail when SELinux MLS policy is enforced. Install the hwloc policy module to mitigate this issue. Please note, that this module requires the hwloc-dump-hwdata files to be present in /var/run/hwloc.

Prerequisites:

- policycoreutils with SELinux scripts
- selinux-devel to build policy.

Use the following command to check if the hwloc module is installed:

```
$ semodule -l | grep hwloc
```

Build it manually in case it is missing from your system. It is required to obtain the policy from the SELinux repo:

```
$ git clone \https://github.com/TresysTechnology/refpolicy-contrib
$ cd refpolicy-contrib
$ make -f /usr/share/selinux/devel/Makefile hwloc.pp
```

Run the following command to install the module:

```
$ semodule -i ./hwloc.pp
```
6. Performance comparisons between RHEL* 7.2 and SLES* 12 SP1 based on the STREAM benchmark revealed that memory transfers to/from MCDRAM in SLES* are ~4% faster:
   - SLES* 490 GB/s
   - RHEL* 470 GB/s

Booting RHEL* 7.2 in the tickless mode will rectify this difference. For more information please see the tickless_xppsl.pdf document.

7. The xppsl-micperf-1.4.1 package cannot be upgraded to xppsl-micperf-1.4.2 or above using yum or zypper. It is necessary to remove the package completely prior to installing a new version (refer to Section 3.4.4 for installation instructions).

   RHEL*/CentOS*:
   ```
   # yum remove xppsl-micperf
   ```

   SLES*:
   ```
   # zypper rm xppsl-micperf
   ```

8. The hwloc service requires the dmi-sysfs Linux kernel module to be loaded. Create an appropriate entry in /etc/modules-load.d/ to load it automatically. Use the command below.
   ```
   # echo "dmi-sysfs" > /etc/modules-load.d/dmi_sysfs.conf
   ```

5.2 Performance issue in cache memory mode

PROBLEM:

The cache mode design places MCDRAM as a direct mapped cache. On Linux* systems this design causes cache performance degradation over time due to increased number of cache collisions caused by memory fragmentation.

SOLUTION:

Use the page sorting module provided in Intel® Xeon Phi™ Processor Software.

INSTALLATION:

If the Intel® Xeon Phi™ Processor Software is installed and running on your system, the correct module is already installed and can be used; proceed to the “Usage” section.

If your machine is running one of the supported vendor kernels, install the correct kernel module package by following the steps below.

1. Navigate to the directory containing binary packages for Intel® Xeon Phi™ Processor Software.
   ```
   # cd xppsl-<xppsl-version>/<os-version>/rpms/x86_64/
   ```

2. Install the kernel module package:
RHEL*/CENTOS*:

```
# yum install kmod-xppsl-addons-*.x86_64.rpm
```

SUSE*:

```
# zypper install xppsl-addons-kmp-default-*.x86_64.rpm
```

### USAGE:

The module sorts kernel free memory pages lists in a way that further minimizes cache misses when those pages are acquired by user processes. Since the module operates on free pages, it is suggested to employ sorting before running each user application.

Furthermore, due to high memory fragmentation, sorting pages alone may not be sufficient to restore initial performance. That is why it is mandatory to use memory compaction beforehand, which increases the amount of groups of physically-contiguous pages. To achieve best efficiency compaction ought to be used before sorting (see example).

Sorting can be called on-demand similar to the example below:

1. Load the module:
   ```
   # modprobe zonesort_module
   ```

2. Trigger memory compaction:
   ```
   # echo 1 > /proc/sys/vm/compact_memory
   ```

3. Trigger sorting (the call returns once sorting completes):
   ```
   # echo <numa_node*> > /
   /sys/kernel/zone_sort_free_pages/nodeid
   ```

   *- currently numa_node can only be set to 0, for details please refer to section "remarks" below

Alternatively, you can configure sorting to trigger automatically with an interval:

1. Load the module:
   ```
   # modprobe zonesort_module
   ```

2. Set the interval of periodic sorting:
   ```
   # echo <interval_in_sec> > /
   /sys/kernel/zone_sort_free_pages/sort_interval
   ```

Note that in case of periodic sorting:

- The action will always be taken on all online nodes. Unlike using `zone_sort_free_pages/nodeid` interface, the node to be sorted cannot be chosen.
Known Issues

- Writing value 0 (zero) disables periodic sorting and cancels all pending activities (if the sorting is ongoing it will finish nonetheless).
- Memory compaction has to be handled by THE system administrator. The module does not call it internally.
- On-demand sorting is disabled. Writing to `zone_sort_free_pages/nodeid` while `zone_sort_free_pages/sort_interval` is set to non-zero value will return `EBUSY`.

**ADMINISTRATION:**

By default, due to security reasons, all interfaces exposed by the module can be written to only by superuser. If the permissions are to be modified it is recommended to do that through the `udev` manager, as in the example below:

1. Create the file `/etc/udev/rules.d/99-zonesort.rules` with the contents:

   ```
   ACTION=="add", DEVPATH=="/module/zonesort_module", SUBSYSTEM=="module", RUN+="/bin/chmod 0666 /sys/kernel/zone_sort_free_pages/sort_interval /sys/kernel/zone_sort_free_pages/nodeid"
   ```

2. Reload the `udev` rules to apply changes:

   ```
   # udevadm control --reload-rules
   ```

   The inserted rule changes access permissions to the interfaces every time the module is being loaded.

**DEBUGGING:**

The module exposes additional interfaces, which may be useful for identifying the state of the running system:

A. `buddy_lists`

   Provides details of the current state of the kernel buddy allocator. In order to use it, dump its contents to a file:

   ```
   # cat /sys/kernel/debug/buddy_lists > output_file
   ```

B. `directmappedcache_state`

   Provides information similar to `/proc/pagetypeinfo` but extended for the purpose of direct mapped cache debugging. The data can be obtained by printing the entry to standard output:

   ```
   # cat /sys/kernel/debug/directmappedcache_state
   ```

For further details on how to interpret the results please refer to the source code of the module, which is delivered with the Intel® Xeon Phi™ Processor Software.
REMARKS:

- The module does not support explicitly allocated huge pages.

- The module has been validated for stock kernels of supported OS distributions (see Table 1). There is no guarantee the module will be functional when used with other kernels.

- The module does not support hybrid memory mode in any of the cluster modes.

- SNC4 and SNC2 cluster modes in the hybrid and cache memory modes are not supported.
6 Kernel Support for Intel® Xeon Phi™ Processor x200 Product Family

The Intel® Xeon Phi™ processor x200 product family requires changes to various pieces of the current Linux* distribution; these changes are being released as patches and RPM source/binary packages, providing a specific version of the Linux* kernel, user space libraries and other utilities.

These changes are planned to be released as part of the associated open source projects. In addition, Intel® is working with Linux* vendors to provide support for the processor.

6.1 Overview

Linux* vendors, such as Red Hat* and SUSE*, take the power of open source software and make it available for enterprises through distributions like Red Hat* Enterprise Linux* (RHEL*) [1] or SUSE* Linux* Enterprise Server (SLES*) [2]. In addition to collecting a set of components, Linux* vendors also test and certify their entire distribution and provide support.

A Linux* distribution includes a Linux* kernel, and several other important pieces of open source software such as GNU shell utilities, compilers (gcc, binutils, etc) and tools/libraries (mcelog, hwloc, etc), daemons, the graphical desktop (X server) and bootloaders like GRUB. Individual vendors also include software built in-house by that company. All of these pieces come together as a single product we think of as the operating system (OS). Additionally, companies like Red Hat* and SUSE* patch the source code in their distributions by picking up bug fixes (for functional, performance or security related issues), perform extensive testing to certify the entire distribution, and provide support (assurance) in case their customers encounter problems.

The Linux* upstream kernel from http://www.kernel.org undergoes many changes between the day the base version is selected by a vendor for inclusion in a particular distribution release and the day that release is shipped. Figure 1 tries to depict how a Linux* kernel for a release of a distribution such as RHEL*/SLES* is created.
The URL below captures current and planned RHEL releases along with the specific base Linux* kernel version for each release: https://access.redhat.com/articles/3078

An article discussing how different Linux* vendors construct their distributions can be found at the following URL http://lwn.net/Articles/486304/

### 6.2 Huge pages

#### 6.2.1 Overview

Linux* systems support 2 MB and 1 GB huge pages, which can be allocated at boot or at runtime. Huge pages can significantly increase performance, particularly for large memory and memory-intensive workloads.

When huge pages are allocated during boot time, they are distributed equally between nodes. Runtime allocation allows the system administrator to choose which NUMA node to allocate those pages from. However, runtime page allocation can be more prone to allocation failure than boot time allocation due to memory fragmentation.
6.2.2 Huge Pages on Red Hat® Enterprise Linux®

**Boot time mode:**

1G huge pages on boot-time mode are enabled by default in Red Hat® Enterprise Linux® kernel. To allocate different sizes of huge pages at boot time, use the following command, specifying the number of huge pages. This example allocates four 1GB huge pages and 1024 2MB huge pages:

```
'default_hugepagesz=1G hugepagesz=1G hugepages=4 hugepagesz=2M
hugepages=1024'
```

Change this command line to specify a different number of huge pages to be allocated at boot.

**Runtime mode:**

Huge pages could be also allocated in the runtime mode on RHEL®/CentOS® systems. To allocate them use following command:

```
# echo <number_of_pages> > sys/devices/system/node/node[0-9]*/hugepages/hugepages-<size_in_bytes>/nr_hugepages
```

6.2.3 Huge Pages on SUSE® Linux® Enterprise Server

**Boot time mode:**

The default size of Huge Page in SLES® is 2 MB. Additional configuration is required to enable huge Pages bigger than the default size. Boot time mode distributes huge pages equally between the nodes.

To allocate different sizes of huge pages at boot time, use the following kernel boot parameters, specifying the number of huge pages. This example allocates four 1GB huge pages and 1024 2MB huge pages:

```
'hugepagesz=1G hugepagesz=1G hugepages=4 hugepagesz=2M
hugepages=1024'
```

Change this command line to specify a different number of huge pages to be allocated at boot.

**Runtime mode:**

Be advised, that default SLES® kernel does not support huge pages in real-time mode. To enable this feature it is necessary to install additional kernel patches, and rebuild kernel with following lines in the kernel `config`:

```
CONFIG_CMA=y
CONFIG_CMA_DEBUG=n
```
Patches to apply:

<table>
<thead>
<tr>
<th>Kernel Commit SHA</th>
<th>Patch name</th>
</tr>
</thead>
<tbody>
<tr>
<td>bae7f4a</td>
<td>hugetlb: add hstate_is_gigantic()</td>
</tr>
<tr>
<td>a7407a2</td>
<td>hugetlb: update_and_free_page(): don't clear PG_reserved bit</td>
</tr>
<tr>
<td>1cac6f2</td>
<td>hugetlb: move helpers up in the file</td>
</tr>
<tr>
<td>944d9fe</td>
<td>hugetlb: add support for gigantic page allocation at runtime</td>
</tr>
</tbody>
</table>

To allocate huge pages use following command:

```
# echo <number_of_pages> > sys/devices/system/node/node \  
[0-9]*/hugepages/hugepages-<size_in_bytes>/nr_hugepages
```

### 6.2.4 Allocate all MCDRAM for 1G Pages

To allocate all MCDRAM for 1G pages is necessary to execute the following commands:

- Enter your platform’s BIOS and set the *Treat MCDRAM as Hotplug* node option to enabled.

- Add the "movable_node" kernel command line– it allows a node to have only movable memory. This option allows the following two things: when the system is booting, node full of hotplugable memory can be arranged to have only movable memory so that the whole node can be hot-removed (specifying the movable_node boot option is required). Once the system is up, the option allows users to online all the memory of a node as movable memory so that the whole node can be hot-removed. Users who do not use the memory hotplug feature can leave this option on since they do not specify movable_node boot option, or they do not online memory as movable.
7 User Space Components not delivered with Intel® Xeon Phi™ Processor Software

7.1 Development Tools

User space components like gcc, binutils and gdb have been updated to include support for AVX-512 code. However, the versions of these components shipped in a Linux* distribution are selected by the Linux* vendor and might not include the updated versions. Consult sections below for further assistance.

7.1.1 Intel® Xeon Phi™ Processor Enabled OS Distribution Versions

RHEL* will have full user space support for AVX-512 processor features. The customer will get support from the Linux* vendor and receive any qualifications required from that vendor.

7.1.1.1 Red Hat Developer Toolset (DTS) Version 3 or later

For customers using Red Hat*, DTS is available at:

https://developers.redhat.com/products/developertoolset/overview/

DTS 3 (and later) provides optional versions of gcc, gdb and binutils. These optional versions are not replacements for the main tools in the distribution, but provide alternate versions of gcc 4.9, binutils 2.24 and gdb 7.8, which are enabled for AVX-512.

7.1.2 Processor Enabled Versions of the User Space Components

The customer can build the open source versions of gcc, binutils and gdb which support AVX-512 and install them as an optional tool chain. By using upstreamed versions, customers can get support for those components from the developer community.

§
The addition of new hardware support provided by an enterprise Linux* distribution is a staged process, where a number of variables come into play. The options provided in this document are not definitive and are meant to serve only as a guide; ultimately the customer needs to decide if any of the options described in this paper fit their needs.
9 References


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